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\$6AU/2188
PATENTS
15311-2321
200308272-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:
Simon G. Steely, Jr. et al.
Serial No.: 09/843,228
Filed: April 26, 2001
For: LOW LATENCY INTER-
REFERENCE ORDERING IN A
MULTIPLE PROCESSOR SYS-
TEM EMPLOYING A MULTI-
PLE-LEVEL INTER-NODE
SWITCH

)
Examiner: Lane, John A.
)
Art Unit: 2188
)

Cesari and McKenna, LLP
88 Black Falcon Avenue
Boston, MA 02210
September 8, 2004

CERTIFICATE OF MAILING

I hereby certify that the following paper is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 8, 2004.

Melissa Altman
Melissa Altman

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT

In response to the Office action mailed June 8, 2004, kindly amend the application as follows:

Amendments to the Specification begin on page 3 of this paper.

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200308272-1

Amendments to the Claims begin on page 4 of this paper.

Remarks begin on page 7 of this paper.